FIG.1

n⁺InGoAs LAYER (EMITTER CAP LAYER) ~	-10
n [†] GɑAs LAYER (EMITTER CAP LAYER) [™]	-9
n In _x Aly Ga _{l-x-y} P LAYER (EMITTER LAYER)	-8
n In _x AlyGa _{1-x-y} P LAYER (CONTROL LAYER)-	-7
p GaAs LAYER (BASE LAYER) —	-6
n GaAs LAYER (COLLECTOR LAYER) —	-5
n ⁺ GaAs LAYER (CONDUCTIVE LAYER)	-4
BUFFER LAYER	-3
GdAs SUBSTRATE —	-2

FIG.2

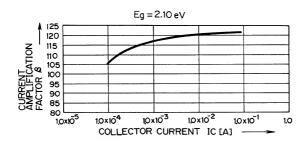


FIG. 3

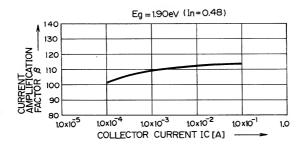


FIG.4

